



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

H. Ikeda et al.

Application No.: 09/917,913

Filed: July 31, 2001

Group Art Unit: 2818

Examiner: C. Yoha

Docket No.: 100353-00065

For: SEMICONDUCTOR MEMORY DEVICE HAVING AN SRAM AND A DRAM ON A SINGLE CHIP

AMENDMENT UNDER 37 C.F.R. § 1.121

Commissioner for Patents
Washington, D.C. 20231

November 18, 2002

Sir:

In response to the Office Action dated July 17, 2002, please kindly consider the following as a full and complete response thereto.

REMARKS

The Office Action dated July 17, 2002 has been received and carefully noted. The following remarks, are submitted as a full and complete response thereto. It should be noted that during a telephone conversation with the Examiner on June 24, 2002, Applicants elected without traverse further prosecution on the invention of Group II, drawn to claims 8-10 and 13-14. Thus, in view of the following remarks, favorable consideration of claims 8-10, 13 and 14 is respectfully requested.

The Office Action rejected claims 8-10 and 13-14 under 35 U.S.C. 103(a) as being unpatentable over Matsui (U.S. Patent No. 6,324,104) in view of Sakata et al. (U.S. Patent No. 5,606,265).

Applicants submit that Matsui is not a proper reference under 103(a). Specifically, Matsui has a patent date of November 27, 2001 with a filing date of March 6, 2000. The present application was filed on July 31, 2001 with a priority date of May 31, 1999. Applicants respectfully submit a verified translation of the Japanese Priority Application.

#6 / Response
w/ Verified
Translation
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L. Spruell